

MAR 17 2004

PTO/SB/08A (08-03)

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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet

1

of

4

Application Number	10/026,078
Filing Date	12/21/2001
First Named Inventor	Matthew Aubury
Art Unit	2123
Examiner Name	Ayal I. Sharon (26214)
Attorney Docket Number	44359/08327 EMB1P072

U. S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

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Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
<i>LS</i>		WO 00 38087 A	06/29/2000	Jonathan Saul	pg 2, ln 5-pg 4 ln 20	
					pg 7, ln 10-lin 15	
					pg 9, ln 2-lin 30	
					pg 21, ln 22-pg 23, ln 13 fig 1, 2	
Examiner Signature	<i>Richard D. Stone</i>			Date Considered	<i>7/22/05</i>	

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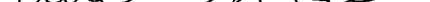
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Sheet	2	of	4
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		Country Code* Number* Kind Code* (if known)				
LS		WO 0038087 A	29-06-2000	AU 12-07-2000	1875200 A	
			GB 07-11-2001	2362005 A		
			WO 29-06-2000	0038087 A1		

Examiner Signature		Date Considered	7/22/05
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**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

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Examiner Name	Ayal I. Sharon
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NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
LB		O'NILS M ET AL: "Design of D-AMPS channel decoder with codesign methodologies" BEC '96. THE 5TH BIENNIAL BALTIC ELECTRONICS CONFERENCE. PROCEEDINGS, BEC '96 5TH BIENNIAL BALTIC ELECTRONICS CONFERENCE. PROCEEDINGS, TALLINN, ESTONIA, 7-11 OCT. 1996, pages 397-400, XP002243970 1996, Tallinn, Estonia, Tallinn Tech. Univ, Estonia ISBN: 9985-59-026-0 the whole document	
LB		YE W ET AL: "Fast timing analysis for hardware-software co-synthesis" COMPUTER DESIGN: VLSI IN COMPUTERS AND PROCESSORS, 1993. ICCD '93. PROCEEDINGS., 1993 IEEE INTERNATIONAL CONFERENCE ON CAMBRIDGE, MA, USA 3-6 OCT. 1993, LOS ALAMITOS, CA, USA, IEEE COMPUT. SOC, 3 October 1993 (1993-10-03), pages 452-457, XP010134577 ISBN: 0-8186-4230-0 the whole document	
LB		HARDT W ET AL: "Speed-up estimation for HW/SW-systems" PROCEEDINGS. FOURTH INTERNATIONAL WORKSHOP ON HARDWARE/SOFTWARE CO-DESIGN (CODE/CASHE '96) (CAT. NO.96TB100020), PROCEEDINGS OF 4TH INTERNATIONAL WORKSHOP ON HARDWARE/SOFTWARE CO-DESIGN.. CODES/CASHE '96, PITTSBURGH, PA, USA, 18-20 MARCH 1996, pages 36-43, XP002243932 1996, Los Alamitos, CA, USA, IEEE Comput. Soc. Press, USA ISBN: 0-8186-7243-9 the whole document	

Examiner Signature	<i>Ayal I. Sharon</i>	Date Considered	7/22/05
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10/026,078

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Matthew Aubury

2123

Ayal T. Sharon L. Osborne

44359/08327 EMBIP072

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EDWARDS M D ET AL: "Software acceleration using programmable hardware devices" IEE PROCEEDINGS: COMPUTERS AND DIGITAL TECHNIQUES, IEE, GB, vol. 143, no. 1, 25 January 1996 (1996-01-25), pages 55-63, XP006006162 ISSN: 1350-2387 the whole document

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